

About the Author

Curriculum Vitae

Personal information

Name: Elisseos Verveniotis
Date of birth: October 24th 1981
Place of Birth: Athens, Greece
Nationality: Hellenic
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Education

2008-2012 Doctoral studies in the Faculty of Mathematics and Physics, Charles University in Prague, Czech Republic.
Study branch: Physics of Nanostructures.
PhD Thesis topic: Structuring and study of electronic and chemical properties of semiconductor surfaces.

July 2008 Graduated from the School of Electronic engineering University of Wales, Bangor, United Kingdom.
Master of Science (M.Sc.) in Nanotechnology and Microfabrication.
Master's Thesis topic: Fabrication of sub-100 nm electrodes for organic electronics.

September 2006 Graduated from the School of Technological Applications, Technological Educational Institute (T.E.I.) of Larissa, Greece.
Bachelor of engineering (B.Eng.) in Informatics and Telecommunications technology.
Bachelor's Thesis topic: Hyper-Threading technology and its applications.

Research interests

SPM and its advanced modes of operation
Hybrid semiconductor electronics involving diamond
Electronic properties of dielectric materials

Languages

Greek, English, Czech, German (passive)